

# BHDL Tool

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*Specification, Test, Contraintes (LIFL)*

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*(INRETS)*

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# Plan

Motivation

HDL / ADL / B

BHDL Tool et Exemple

Les extensions en cours

# Conception de systèmes complexes

## *Métaphore de l'aile d'oiseau*

Éléments : Plumes, squelette,  
vaisseaux, muscles

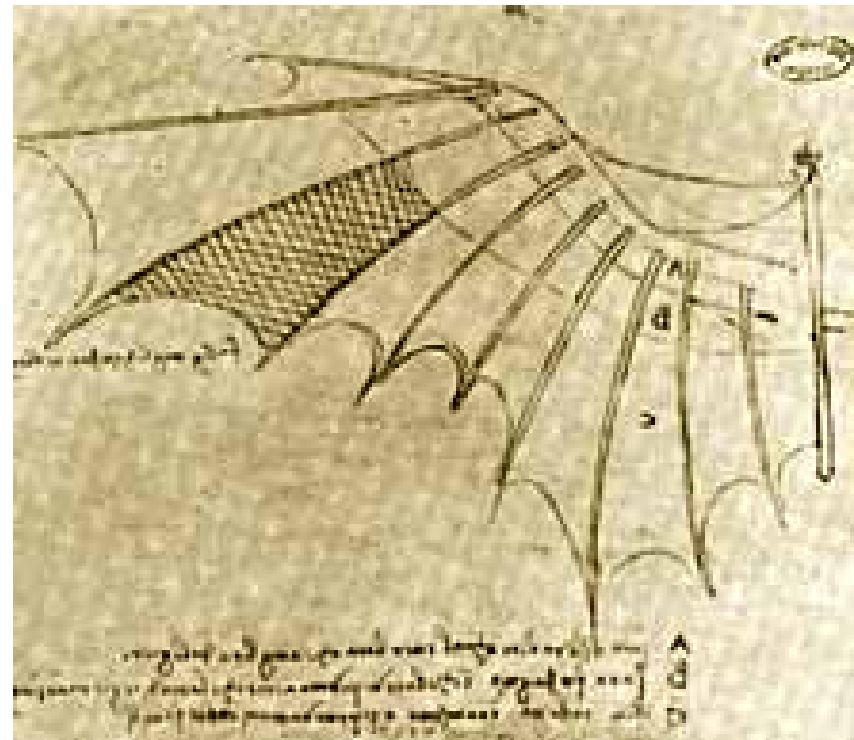
Qualités : Poids,  
aérodynamisme, protection  
thermique, résistance

Généricité : plumes, ...

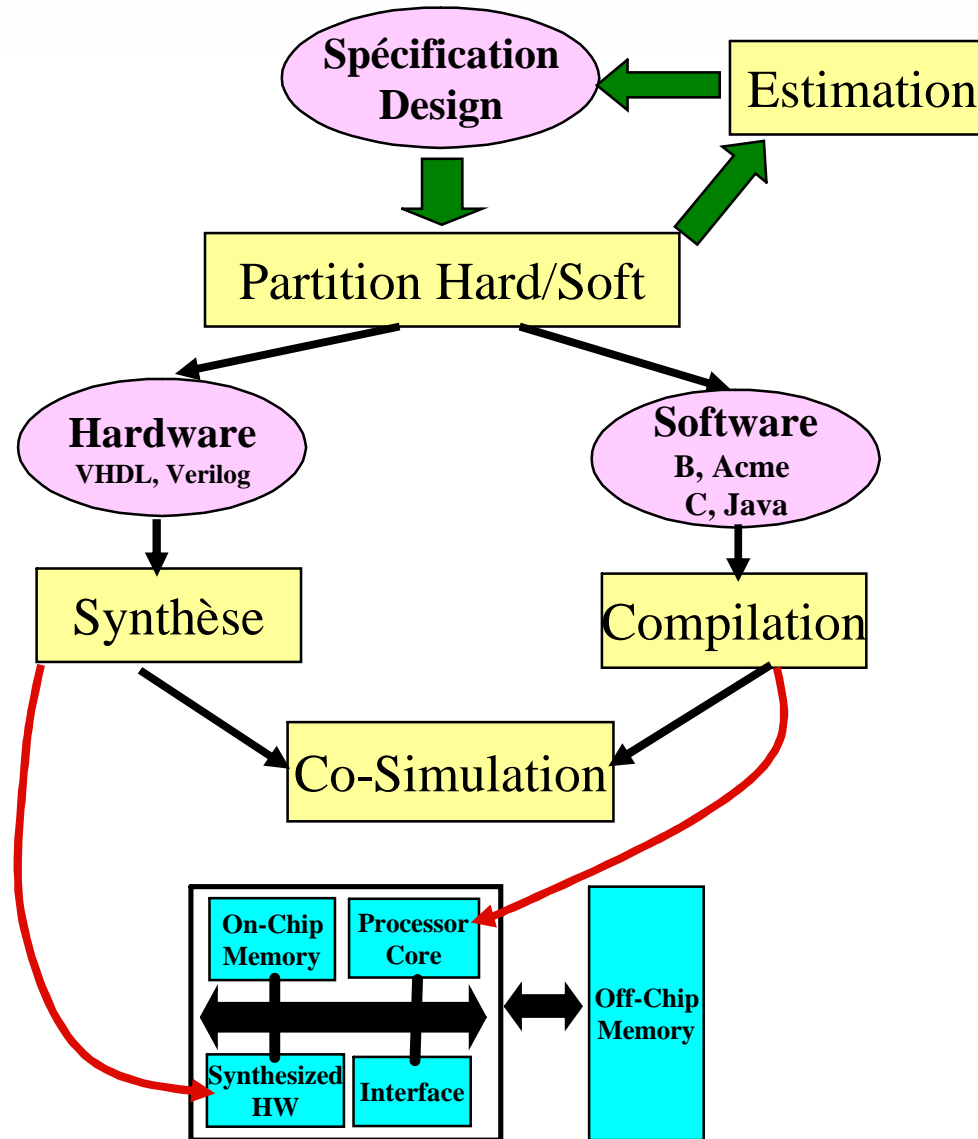
Systeme :

dynamique

≠  $\sum$  Points de vue

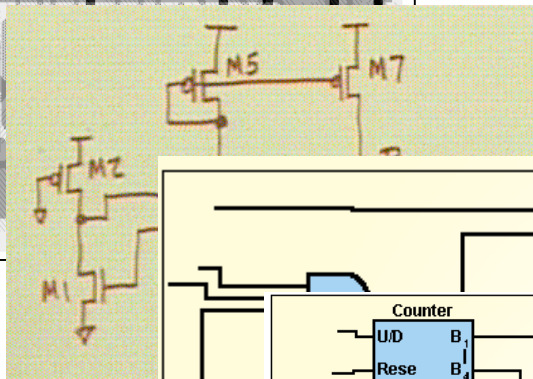
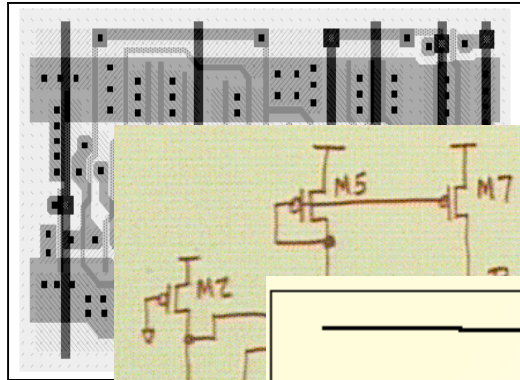


# Co-design / Hardware-Software

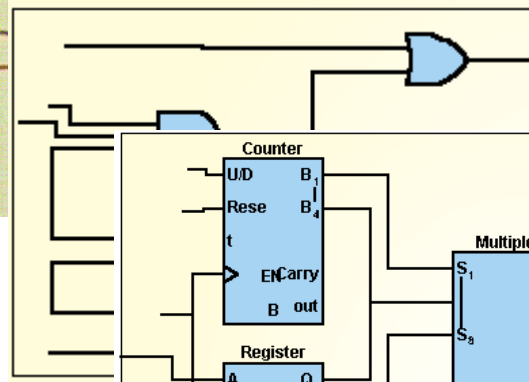


BHDL Tool

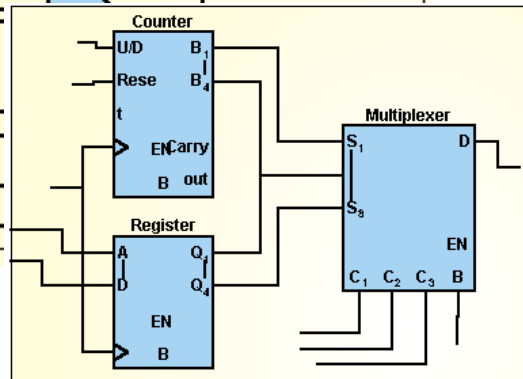
# HDL / ADL / B



Conception physique



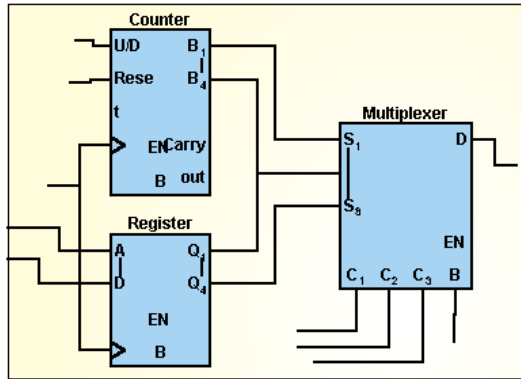
Conception structurelle



Conception logique

Conception fonctionnelle

# HDL / ADL / B



VERILOG – VHDL

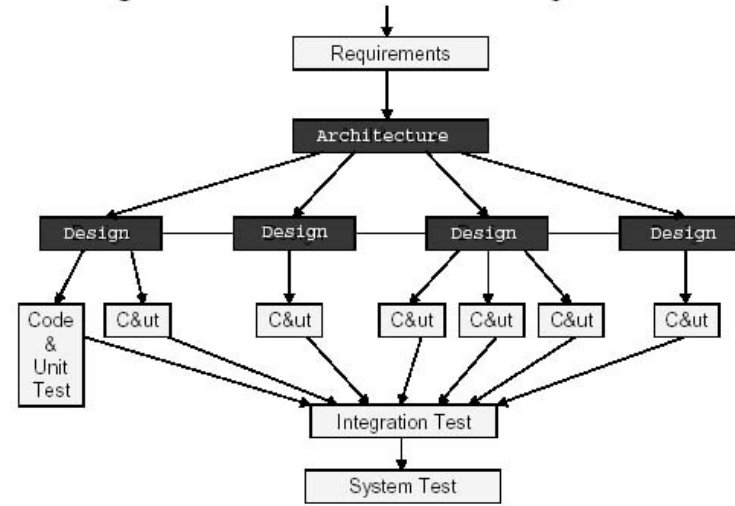
+ Synthèse

+ Réutilisabilité

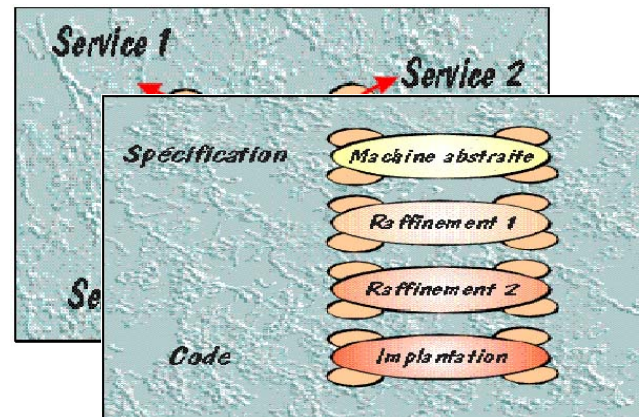
- Simulation

**Méthode B**

*Composant  
Port  
Abstractions  
Hierarchie*



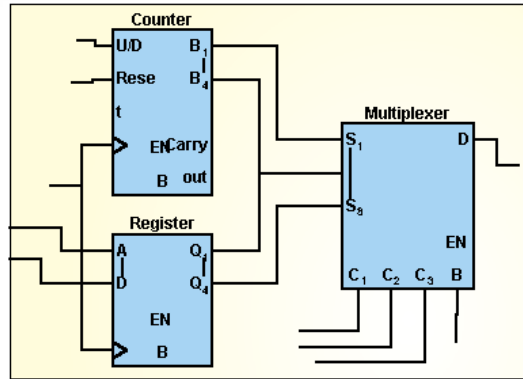
Wright ... **ACME**  
Abstractions



Exigences

Preuves assistées

# HDL / ADL / B

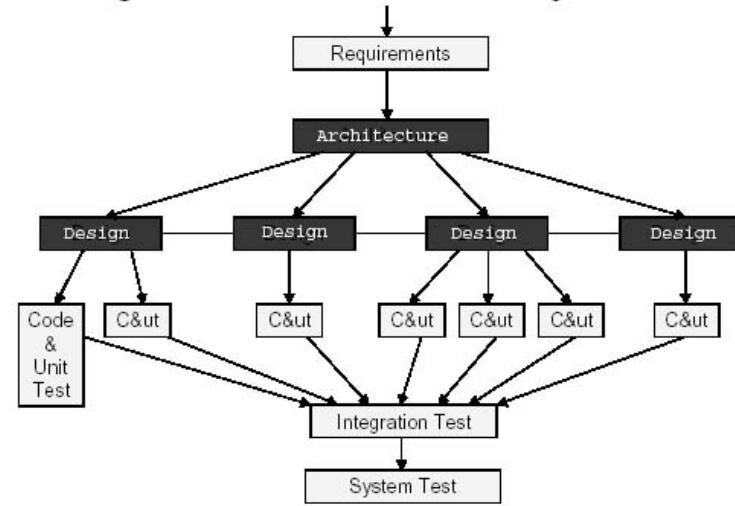


VERILOG – VHDL

- + Synthèse
- + Réutilisabilité
- Simulation

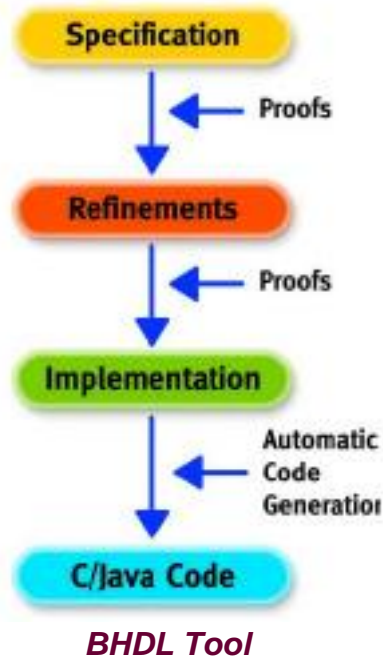
**Méthode B**

*Composant  
Port  
Abstractions  
Hierarchie*



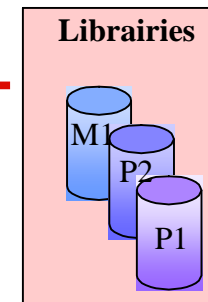
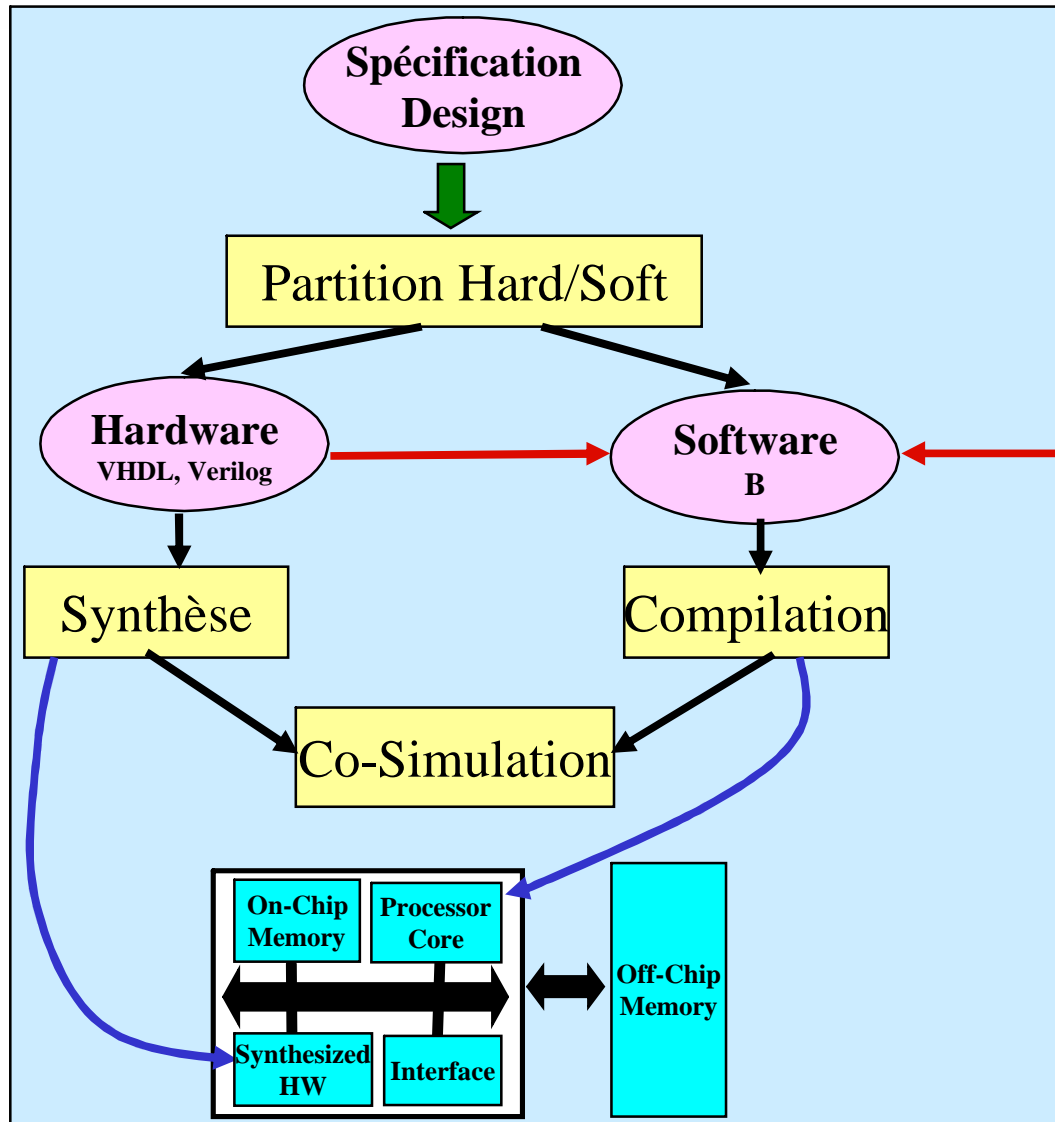
Wright ... **ACME**  
*Abstractions*

Exigences  
Preuves assistées  
*Suret *



*BHDl Tool*

# Co-Design *BHDL*



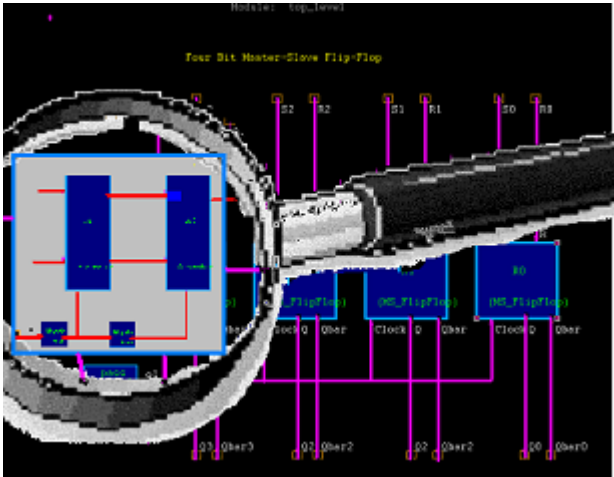
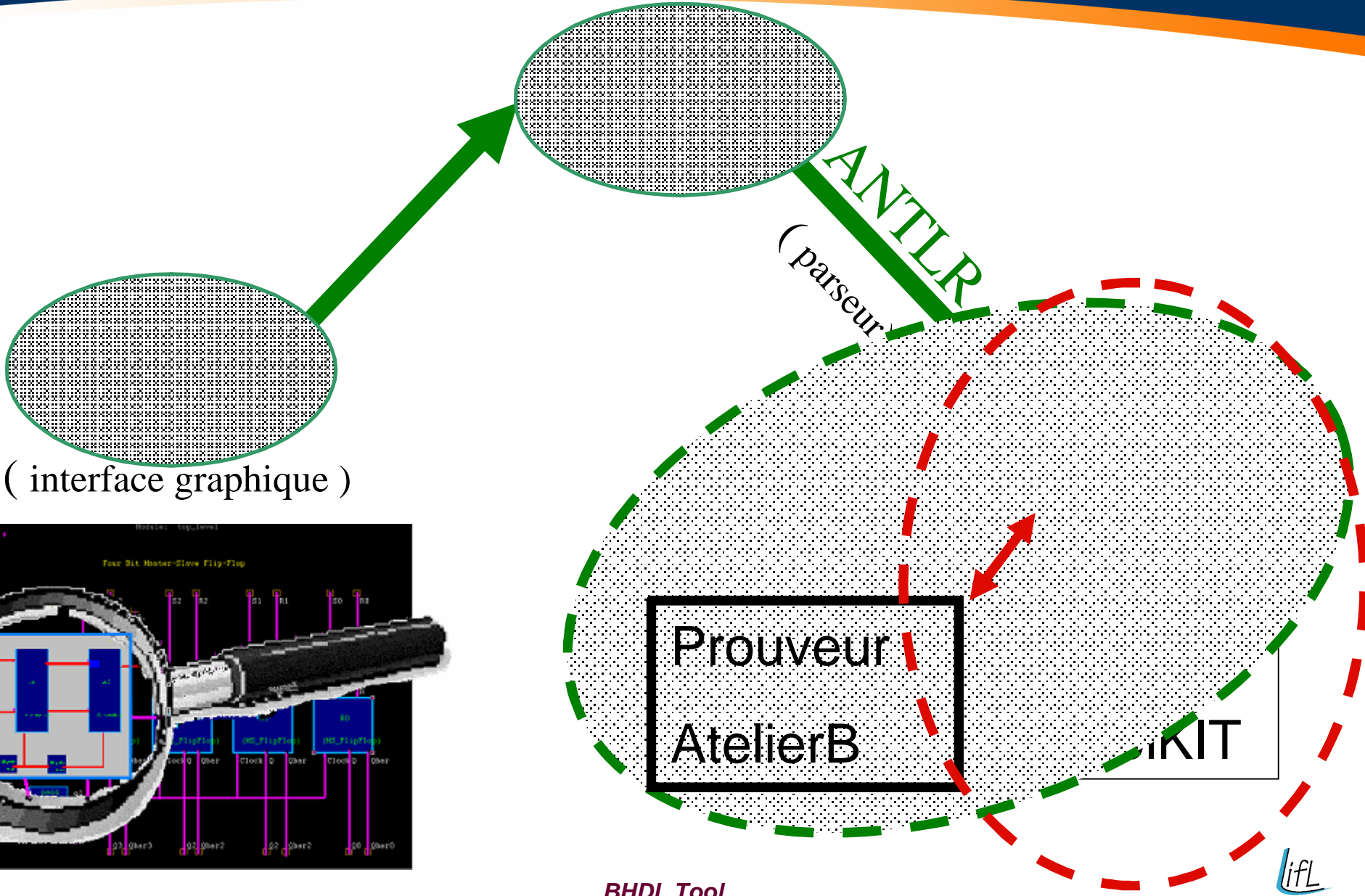
IEEE Standard  
Logic 1164

....

# L'expression HDL / B

- Le code HDL est utilisé pour spécifier l'architecture structurelle et fonctionnelle des composants
- Le code B généré capture
  - ◆ L'architecture structurelle des composants
  - ◆ Le type des données
  - ◆ Les annotations

# BHDL 0.2



# BHDL 0.3

